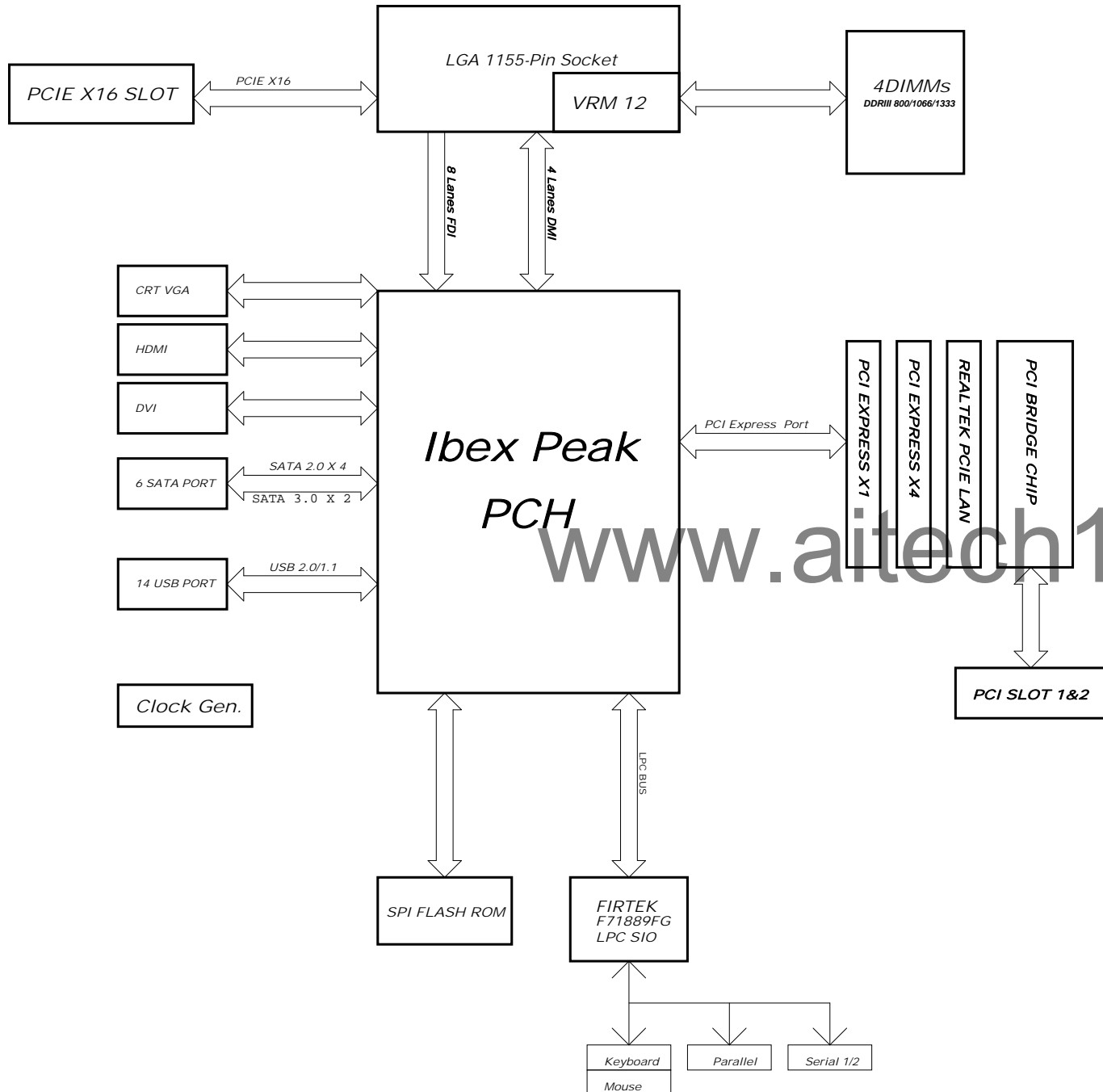
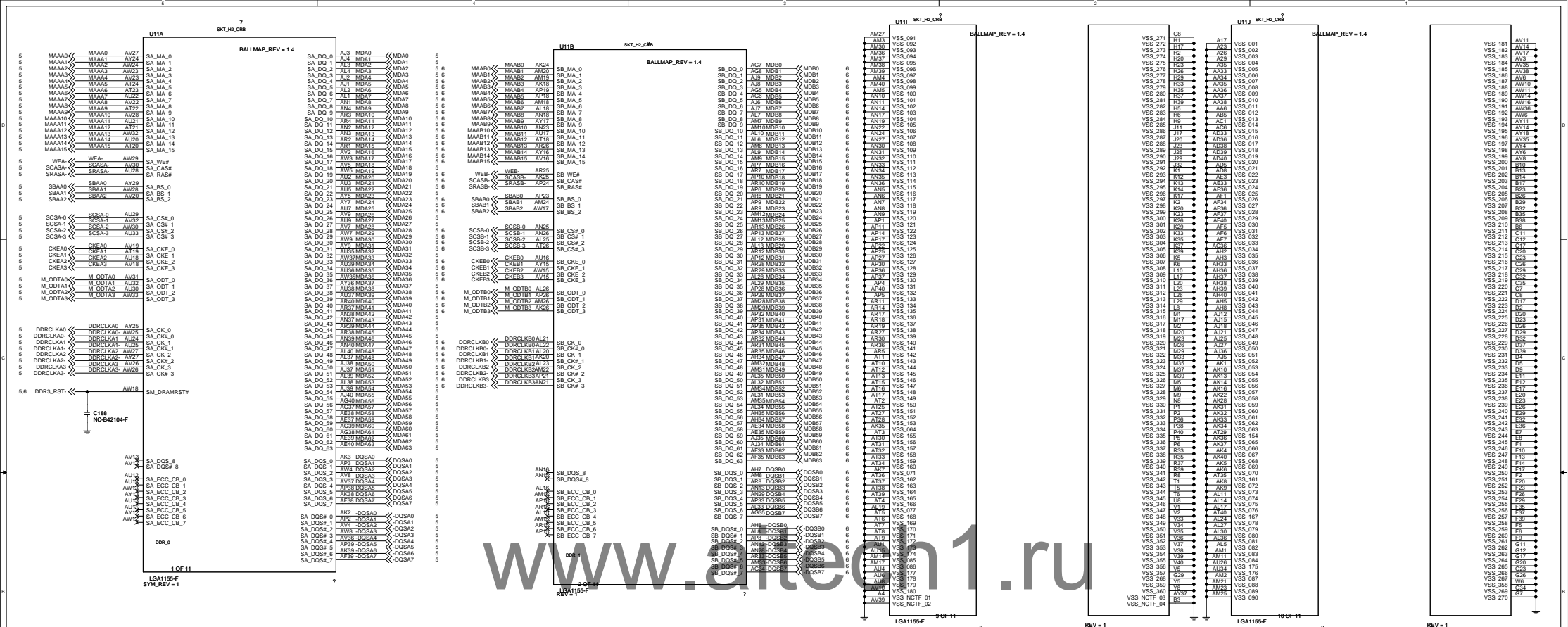


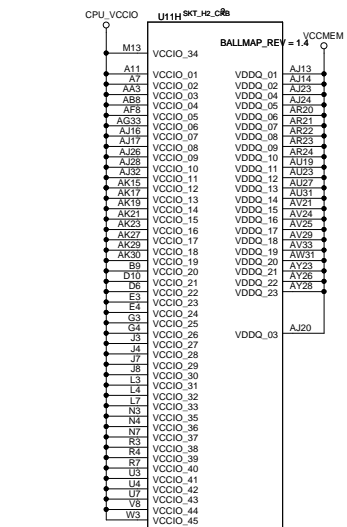
BLOCK DIAGRAM

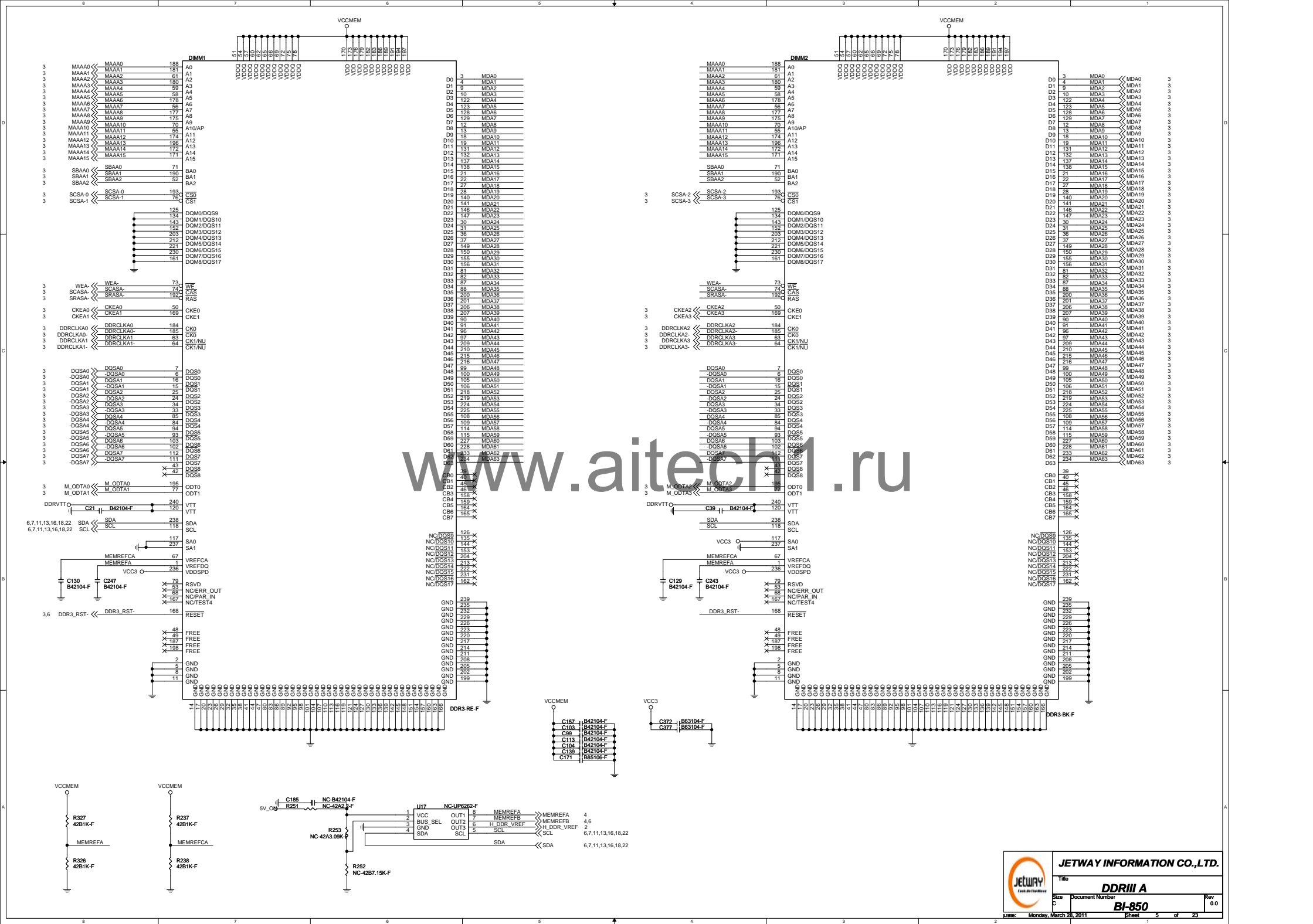
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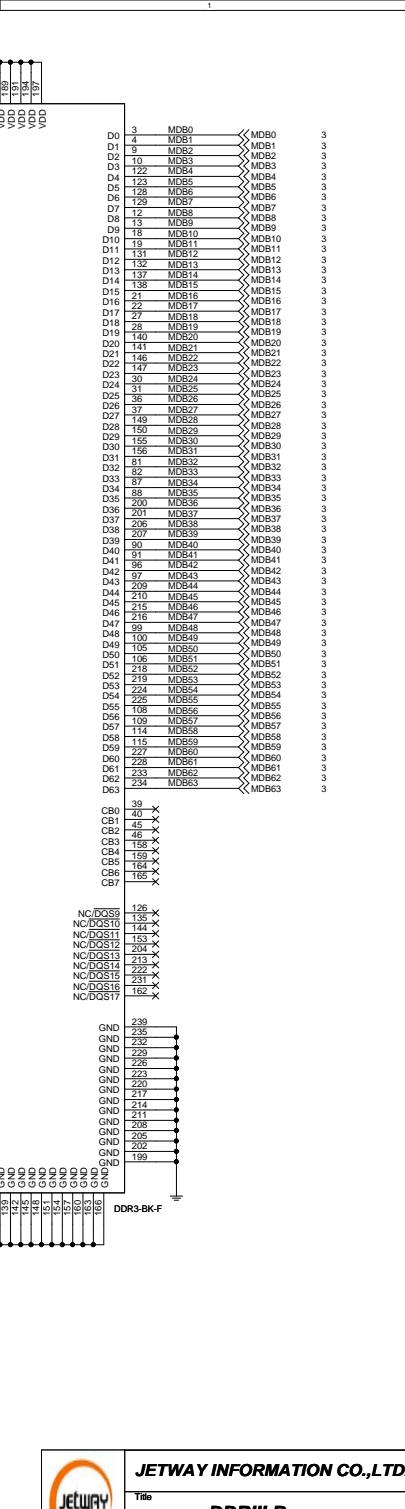
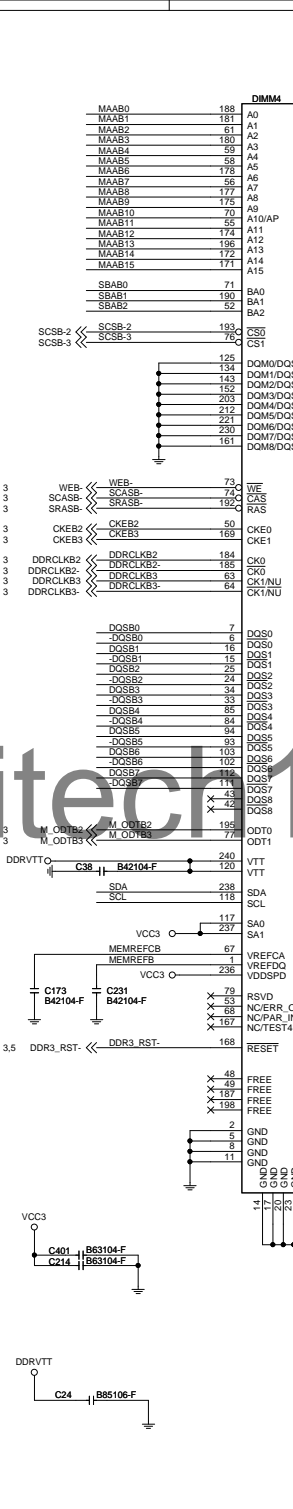
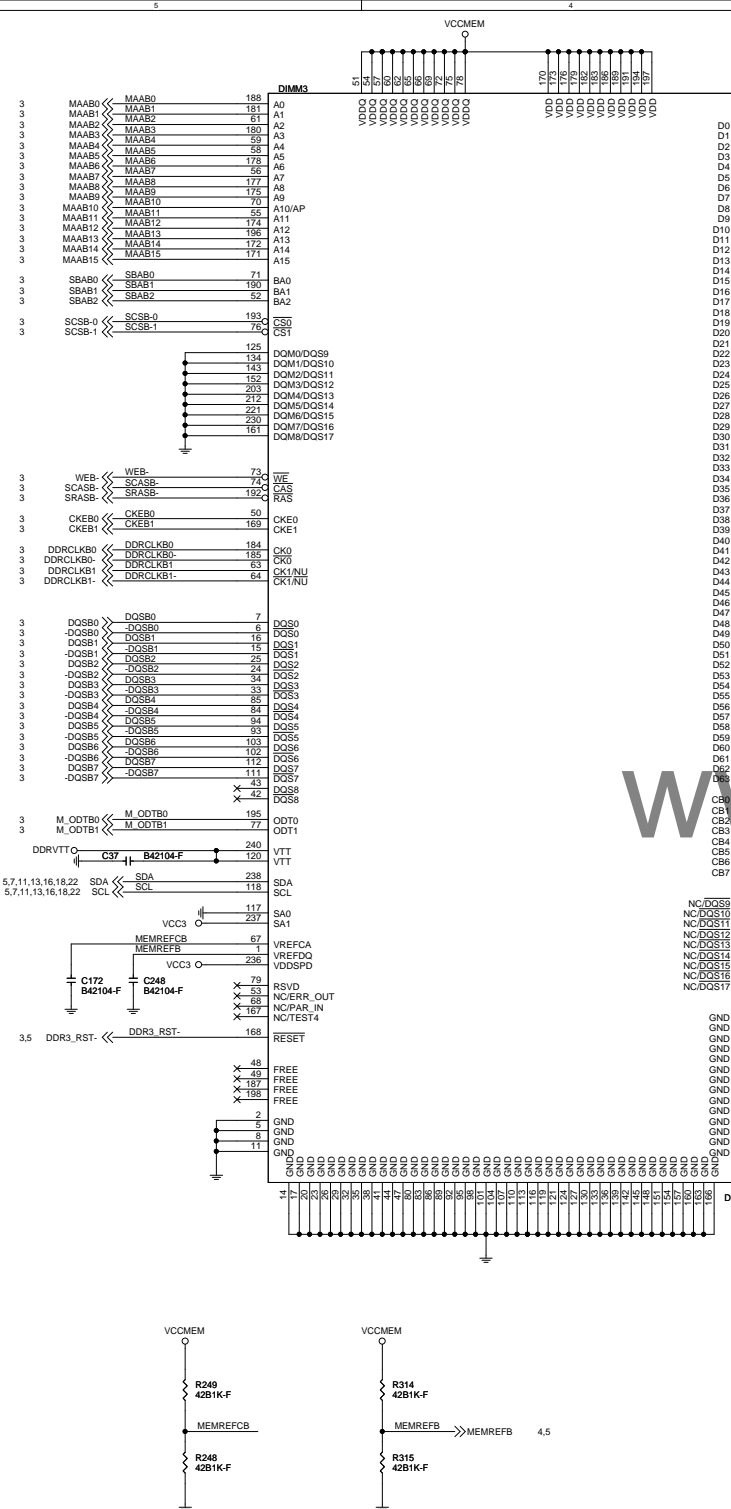
01. BLOCK DIAGRAM
02. CPU MAIN & PCIE
03. CPU MEMORY & GND
04. CPU POWER
05. DDRIII A
06. DDRIII B
07. PCH MAIN
08. PCH CLOCK & DISPLAY
09. PCH POWER & GND
10. LPC SIO & H/W MONITOR
11. PCI & PCIE X1 SLOT & SDVO SLOT
12. HD AUDIO
13. PCIE X 16 SLOT
14. POWER,USB & FRONT CONN.
15. CLOCK GEN.
16. CPU POWER PWM
17. CPU POWER DRIVER
18. SYSTEM POWER I
19. SYSTEM POWER II
20. RTL8111D/8102EL PCI-E LAN

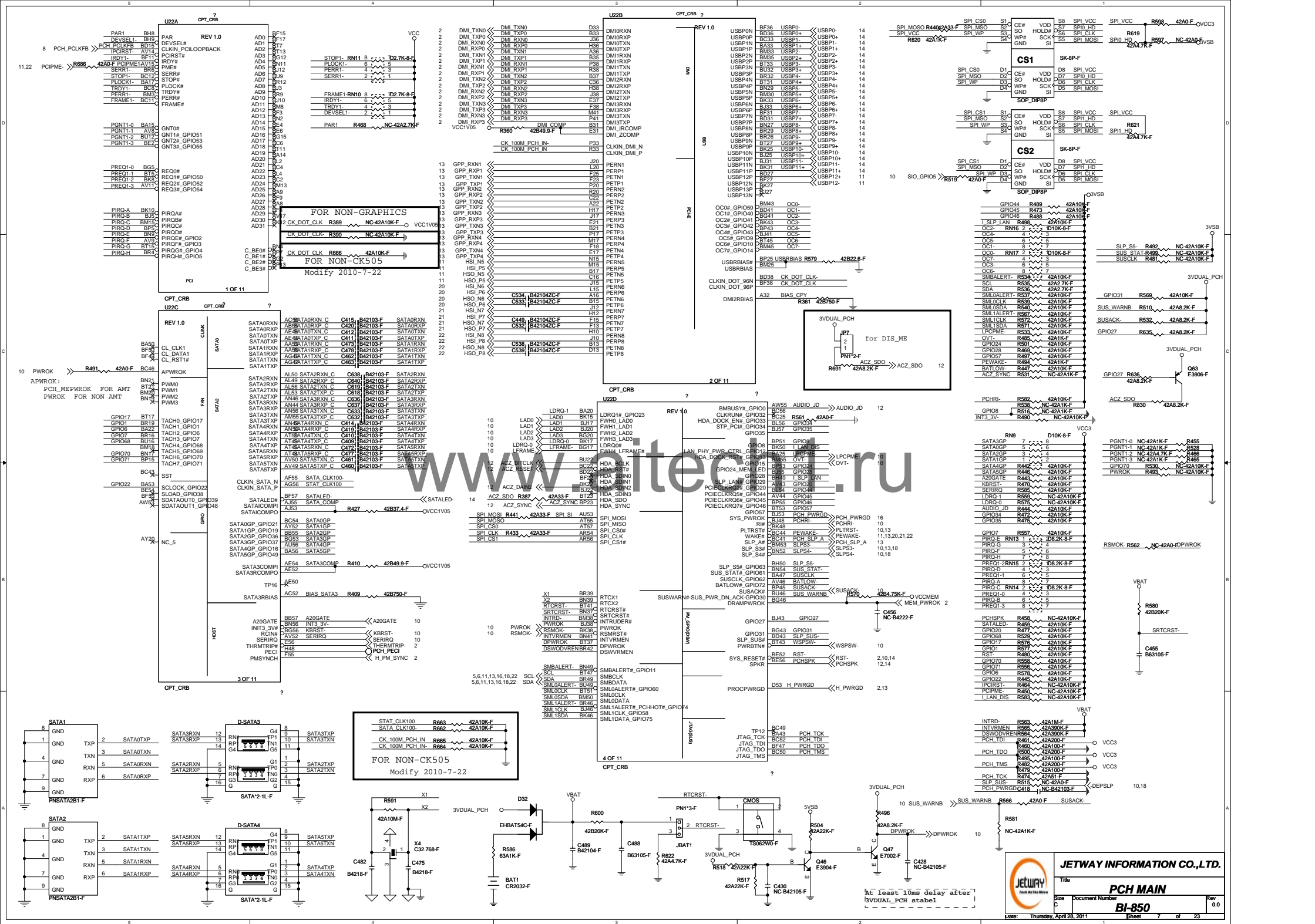


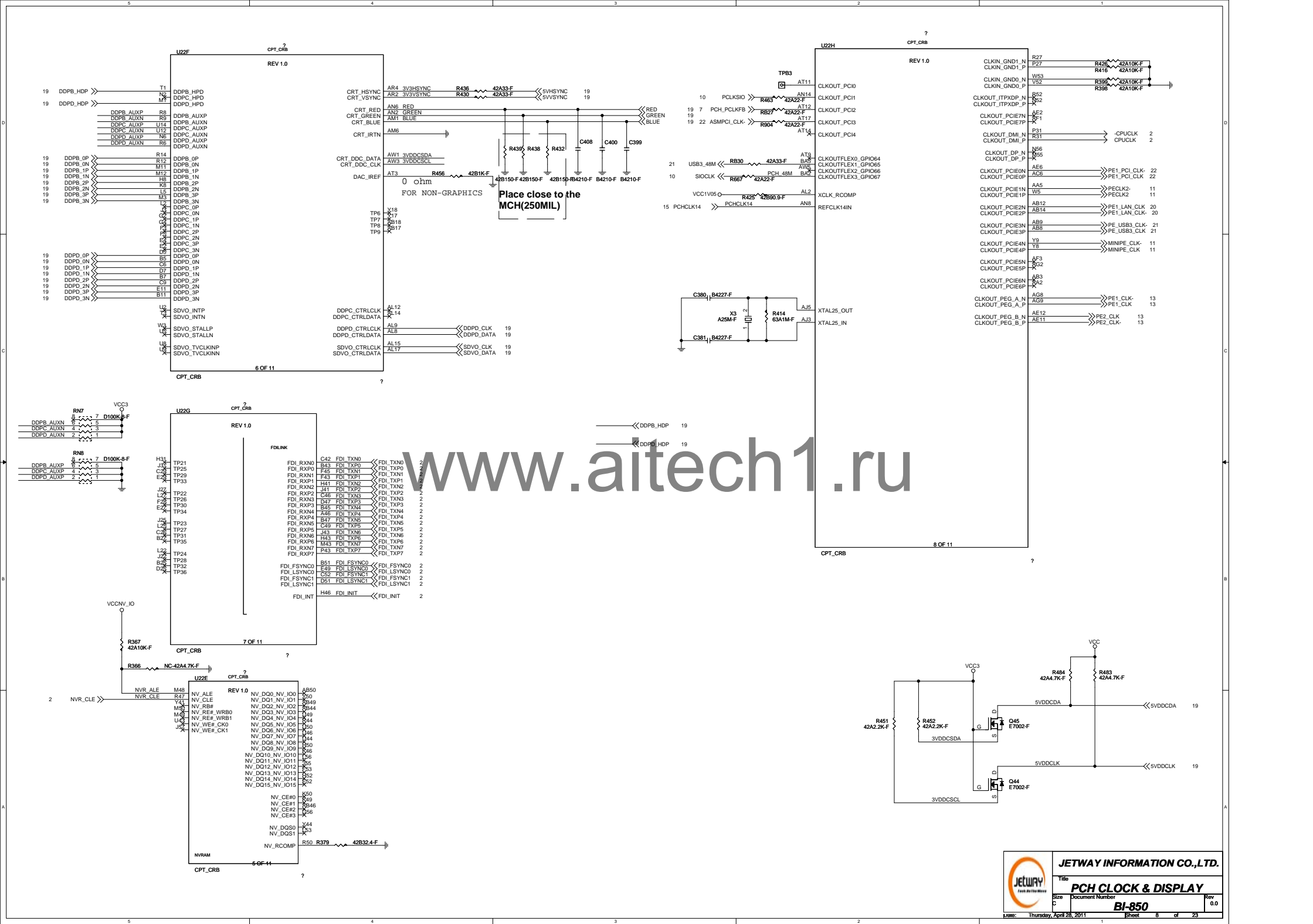


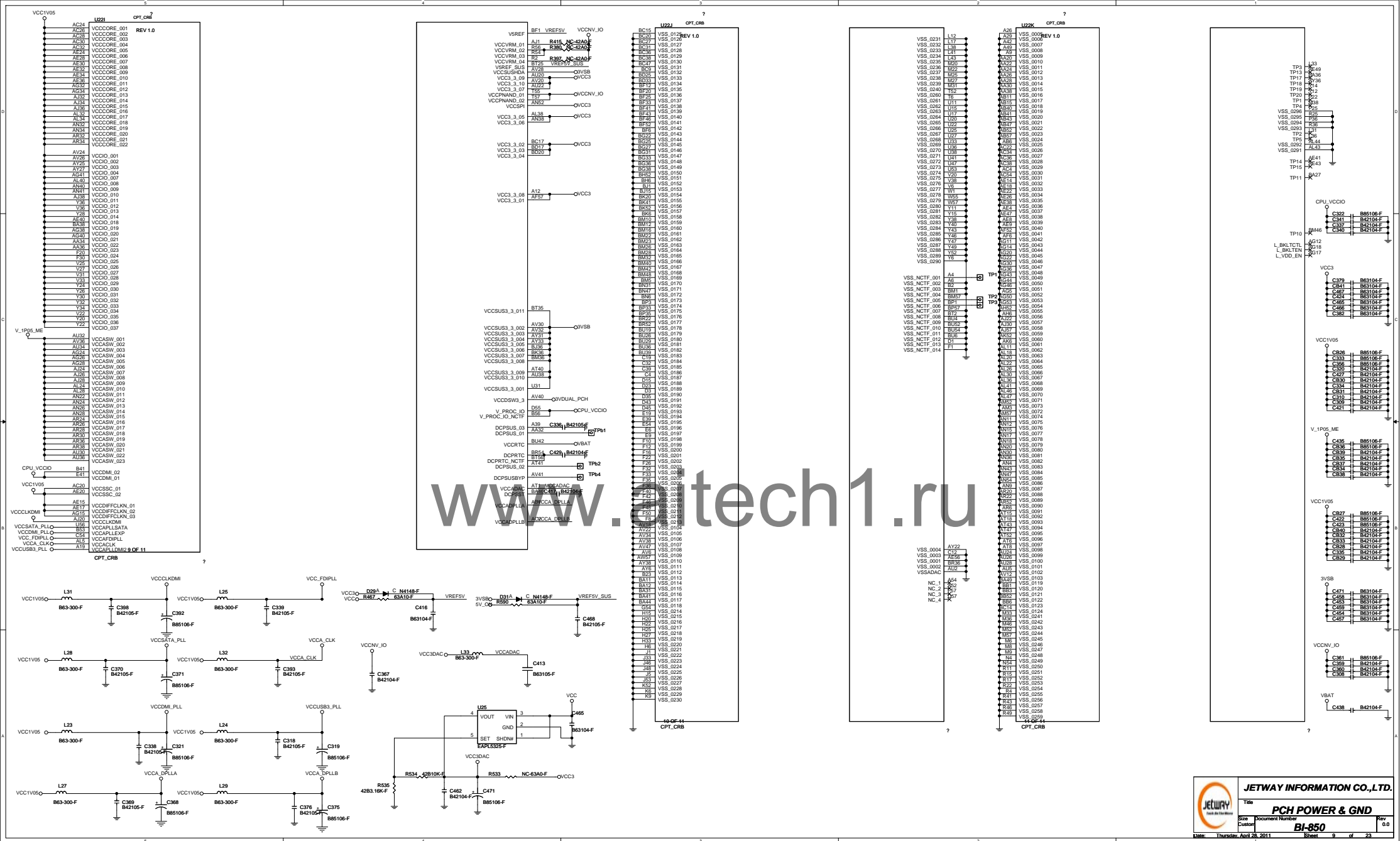












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PIN2   DTR2#  HI:SPI as a back up BIOS(Default)
        LO:SPI as primary BIOS

PIN3   RTS2#  HI:PWM FAN(Default)
        LO:DAC FAN

PIN5   SOUT2   HI:SPI Function Disable. (Default)
        LO:SPI Function Enable.

PIN28  LDRQ#   HI:PIN 51-56 are bus Interface(Default)
        LO:PIN 51-56 are GPIO pin

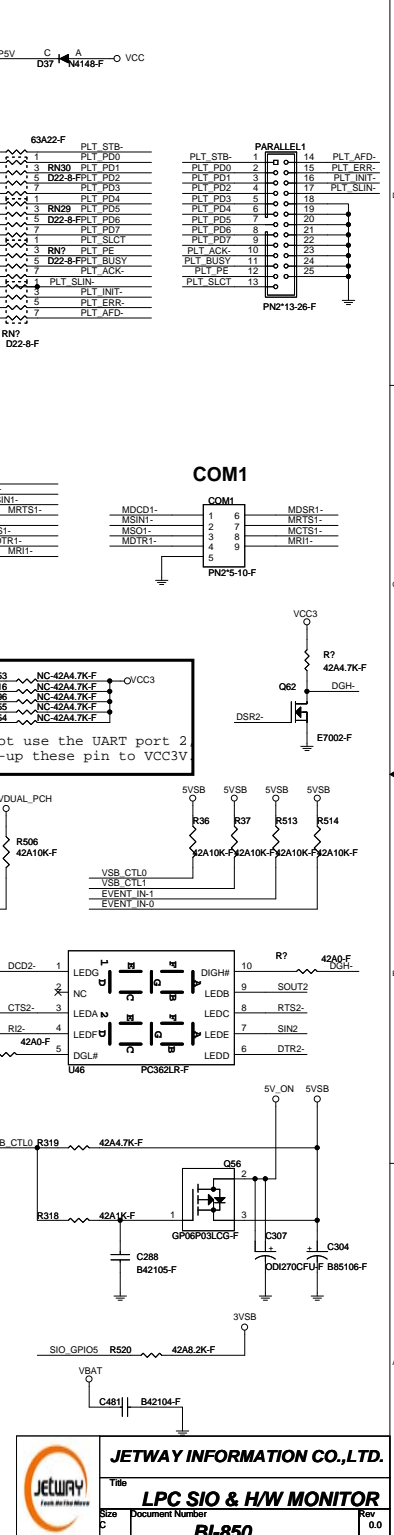
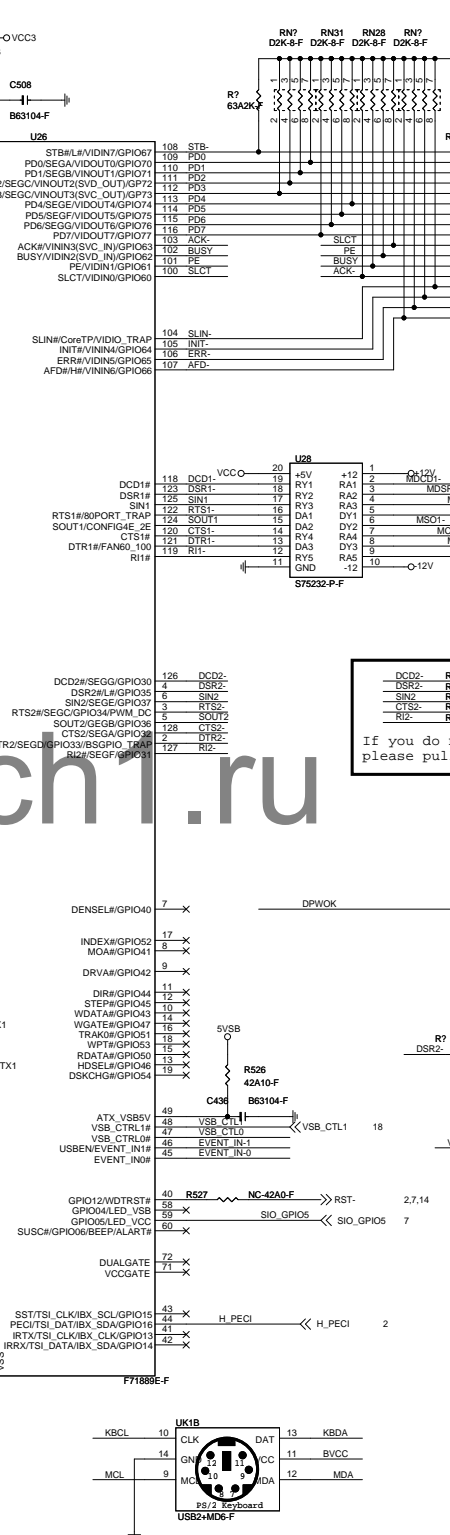
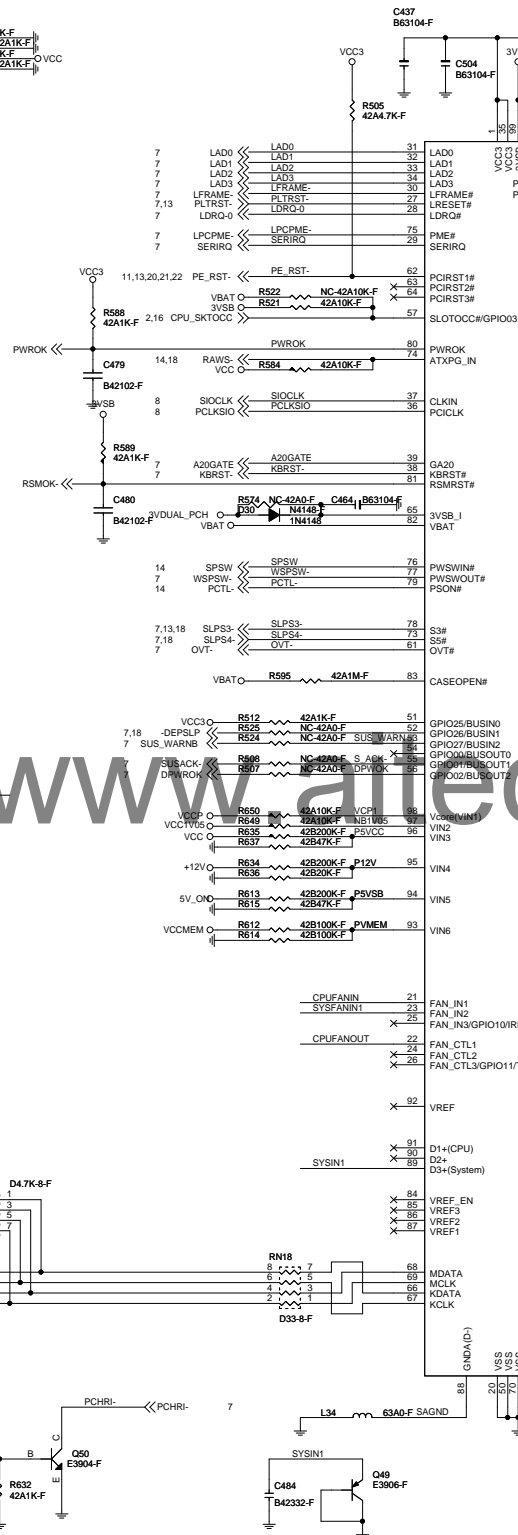
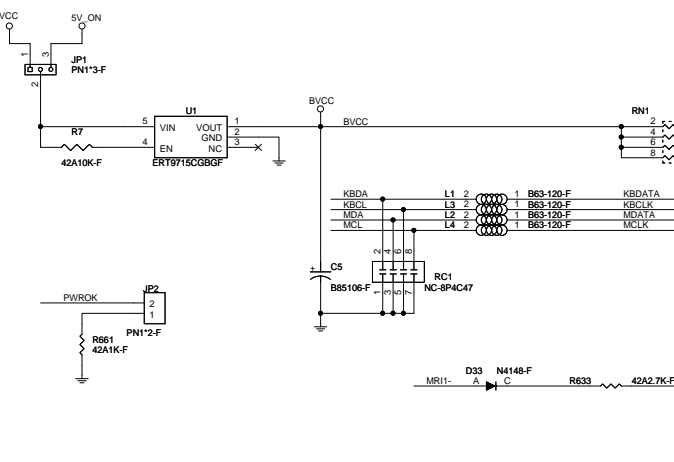
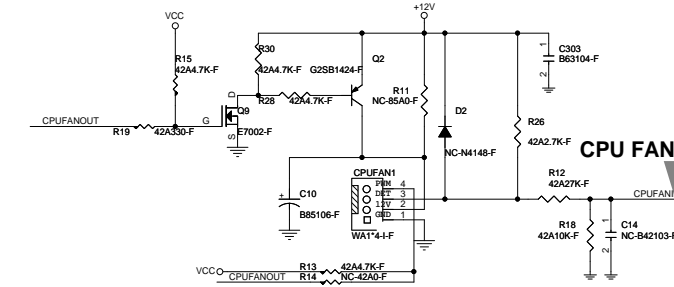
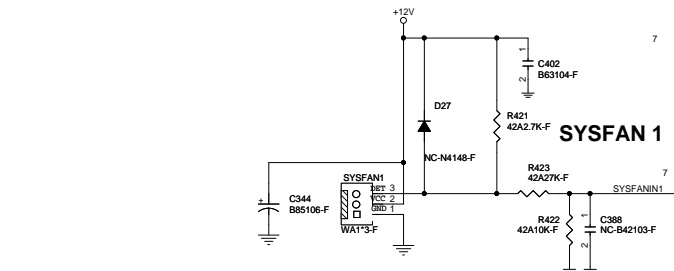
PIN104 SLIN#   Pull High 1K Pin 100-116 as LPT Interfaces
        Pull High 20K Pin 100-116 as FVID Controller
        Pull Down 1K Pin 102/103/111/112 as SVID Controller
        Pull Down 47K Pin 100-103 & 105-116 as GPIO Pin

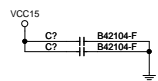
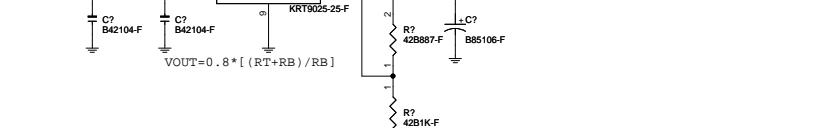
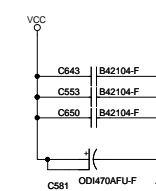
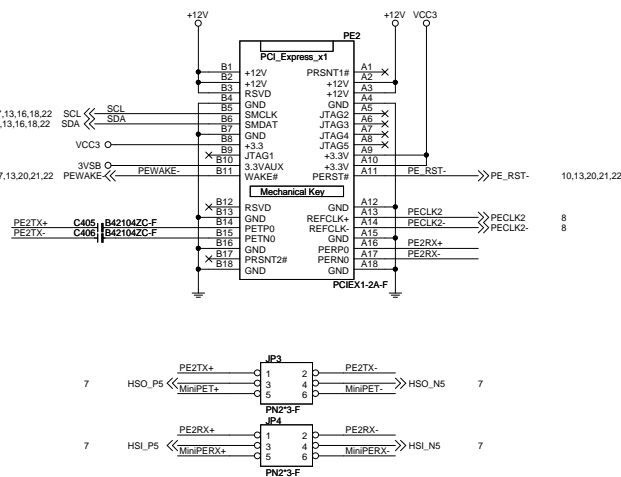
PIN121 DTR1#  HI:FAN Default Duty 60%. (Default)
        LO:FAN Default Duty 100%.

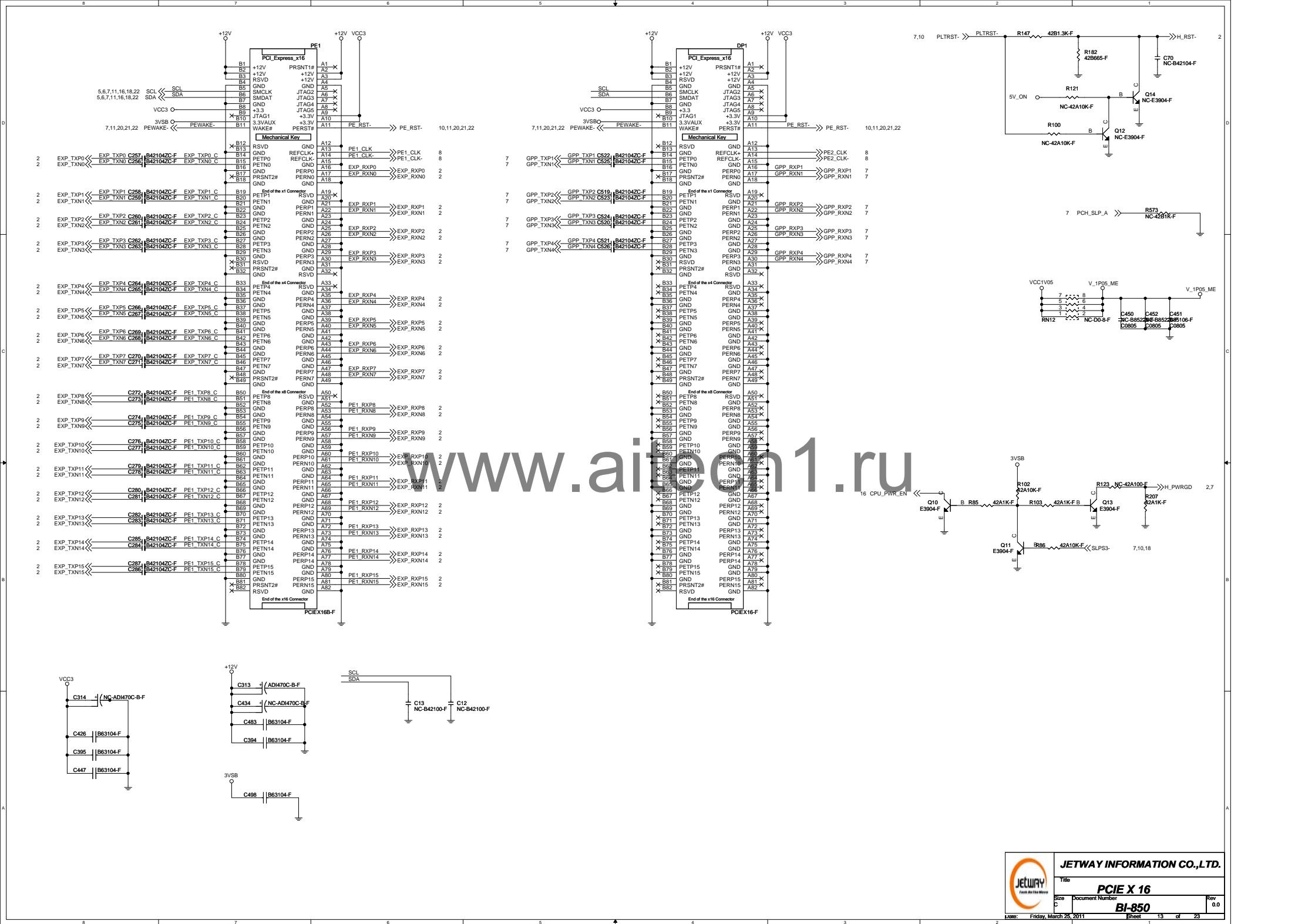
PIN122 RTS1#  HI:80 port decode output form COM2 Interface (Default)
        LO:disable the 80 port function.

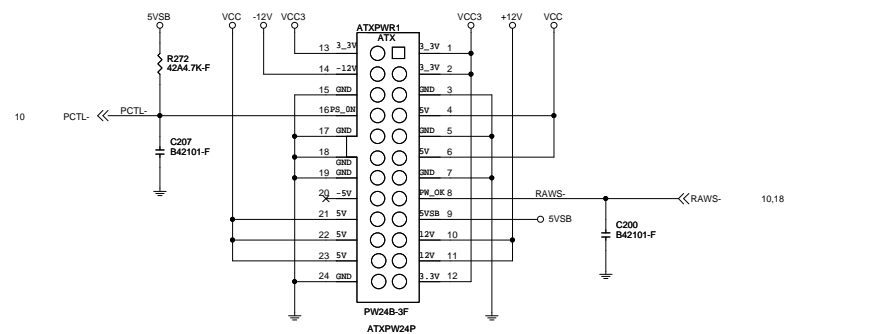
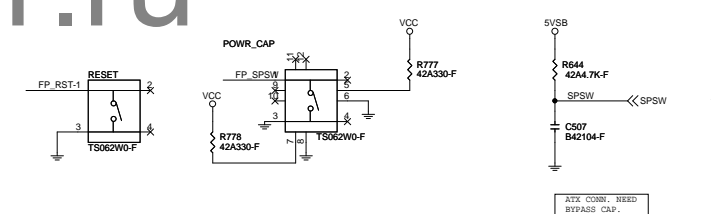
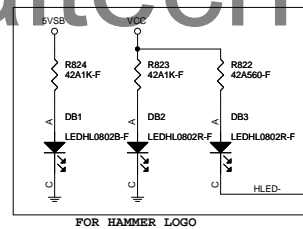
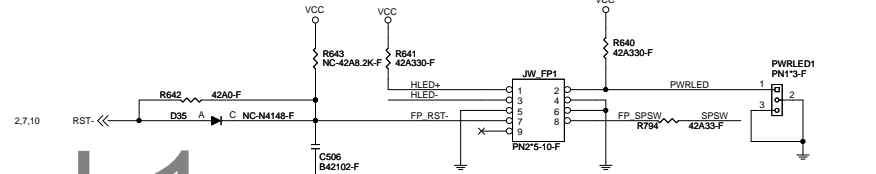
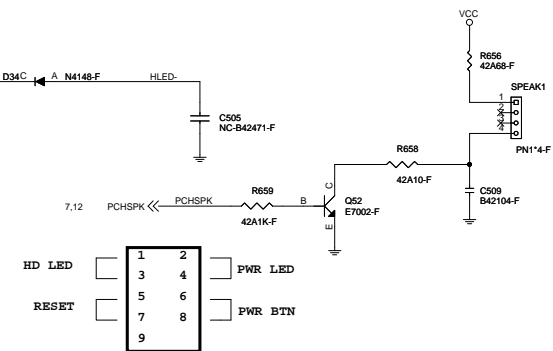
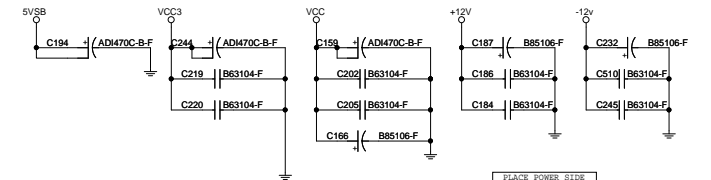
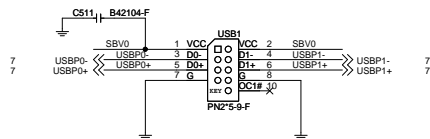
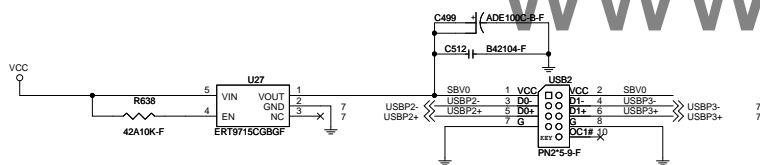
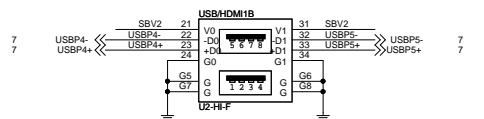
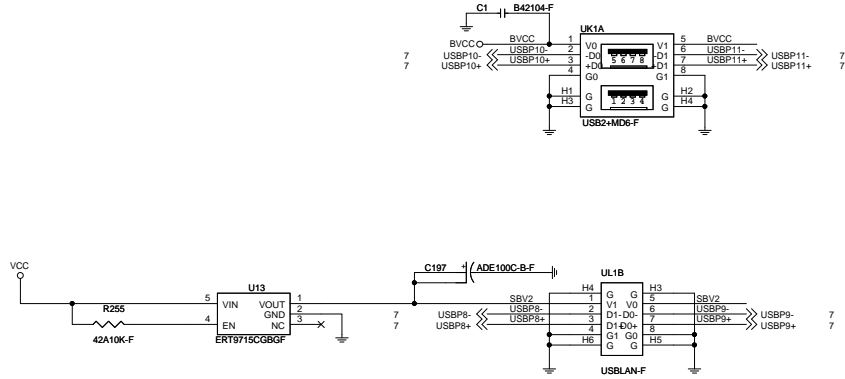
PIN124 SOUT1  HI:4E. (Default)
        LO:2E

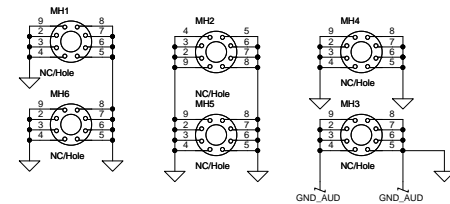
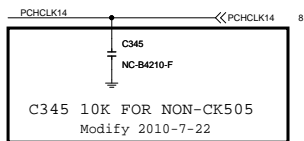
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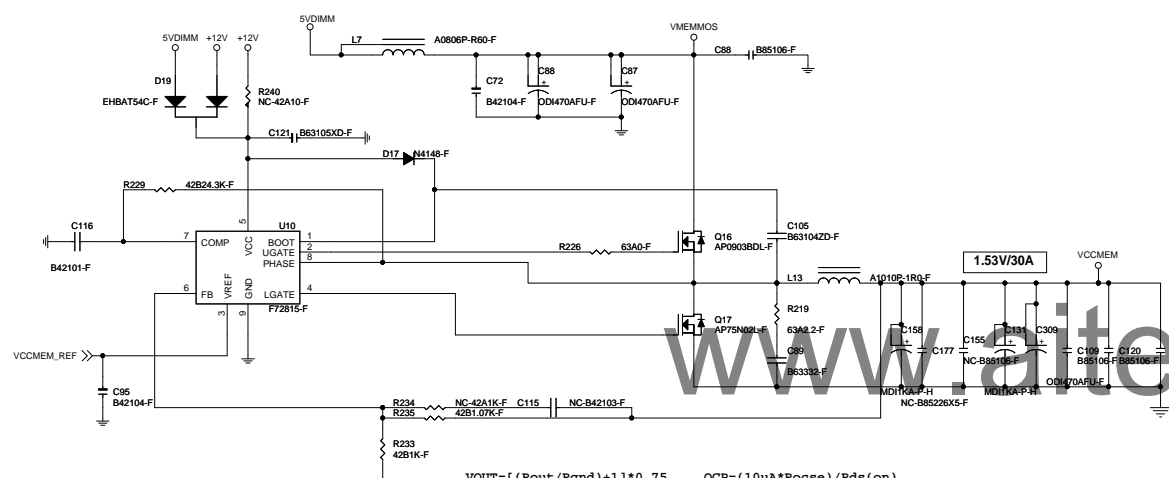
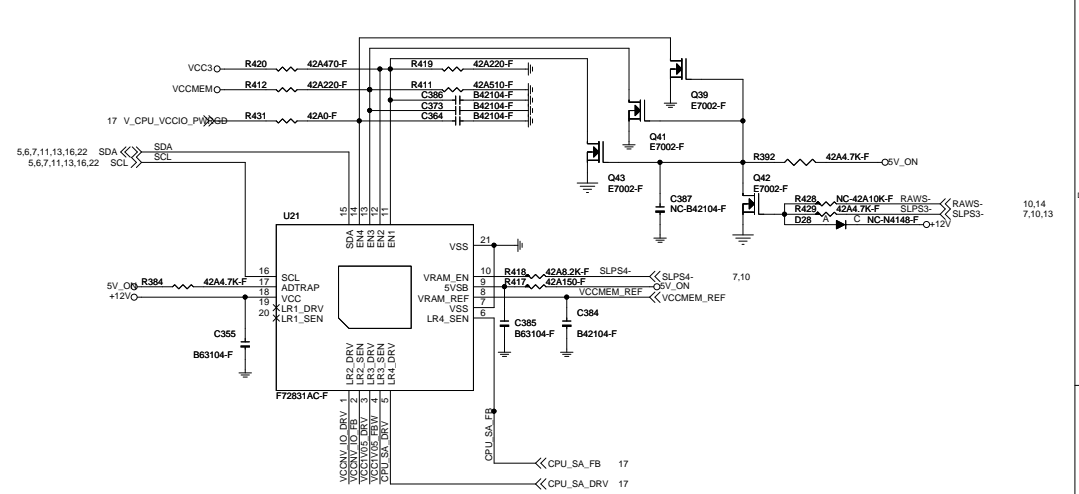
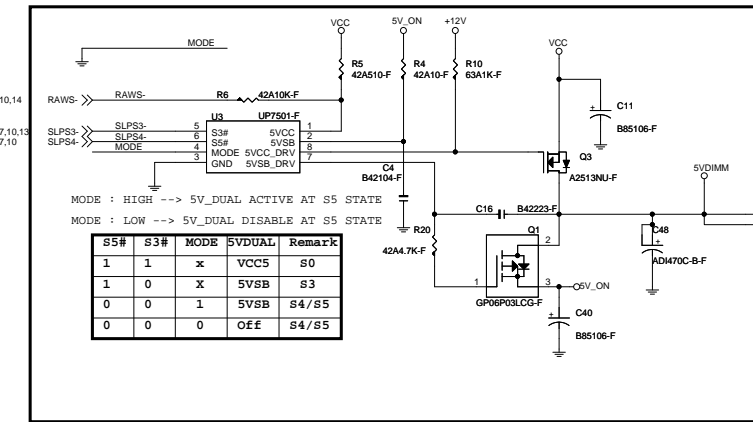




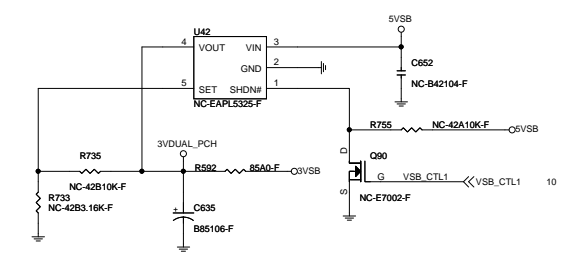
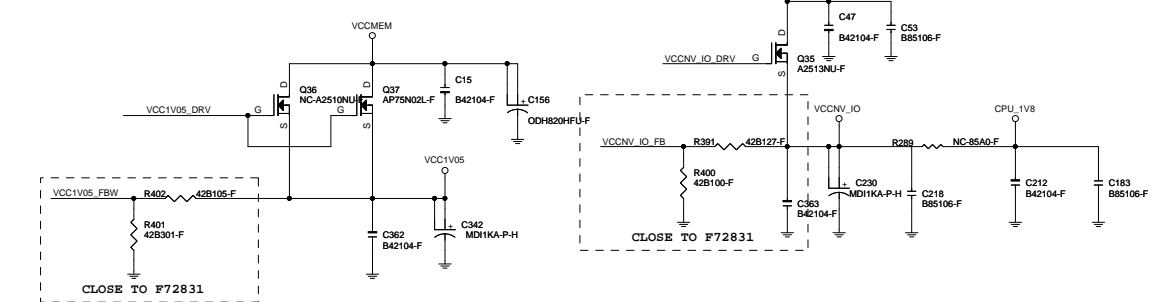
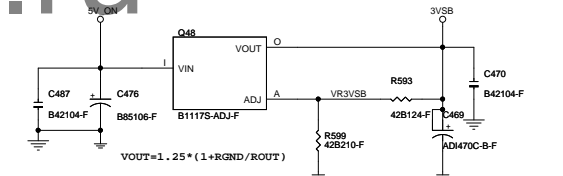


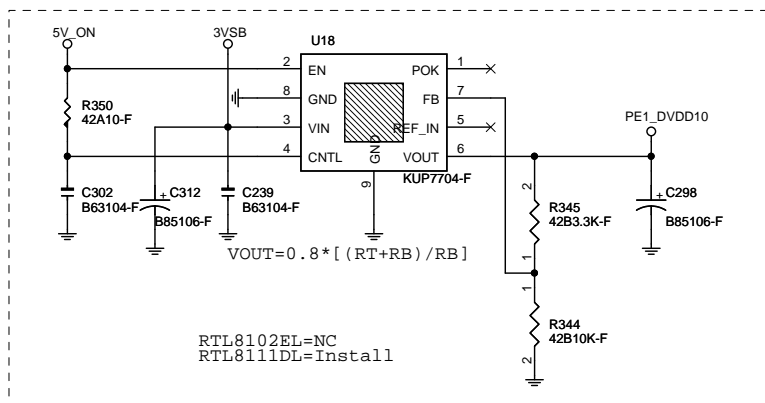


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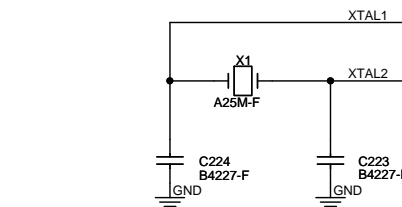
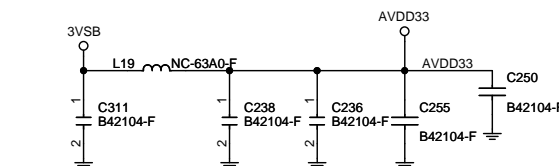
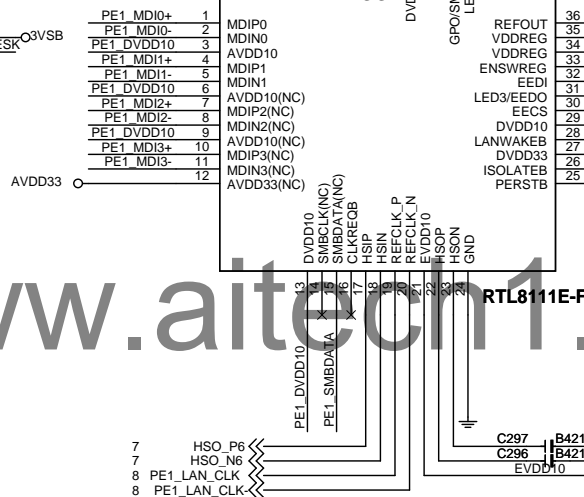
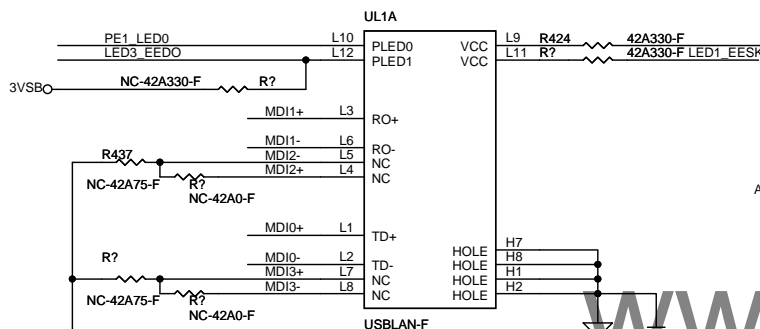
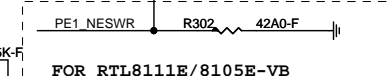
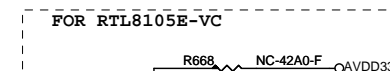
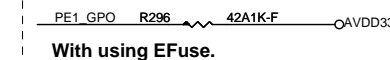
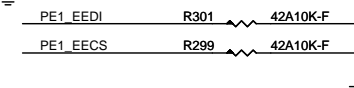
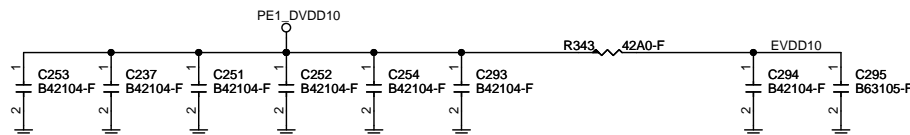


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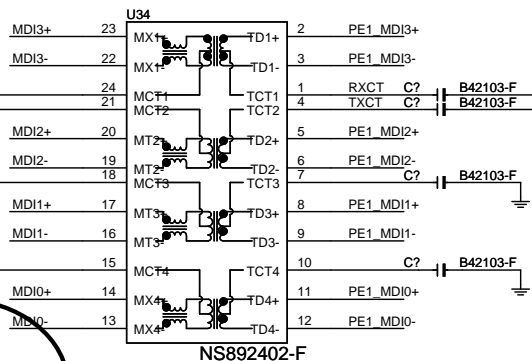




RSET value should be 2.49K (1%) for
RTL810E and RTL8111E application
& USE GND GUARD



FOR 10/100/1000



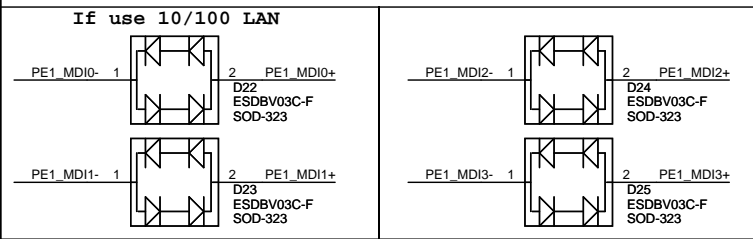
CO-LAY
C? NC-B63104-F
C? NC-SC1KP2KV-F

C651
ESDBS201N-F
C1206

扶棟溫莖奪最輪
Transformer啊溫

If use 10/100 LAN, Change
Transformer to NS892404

If use 1000M LAN
減澤Ultra Low Capacitance TVS最輪LAN CHIPSET啊溫



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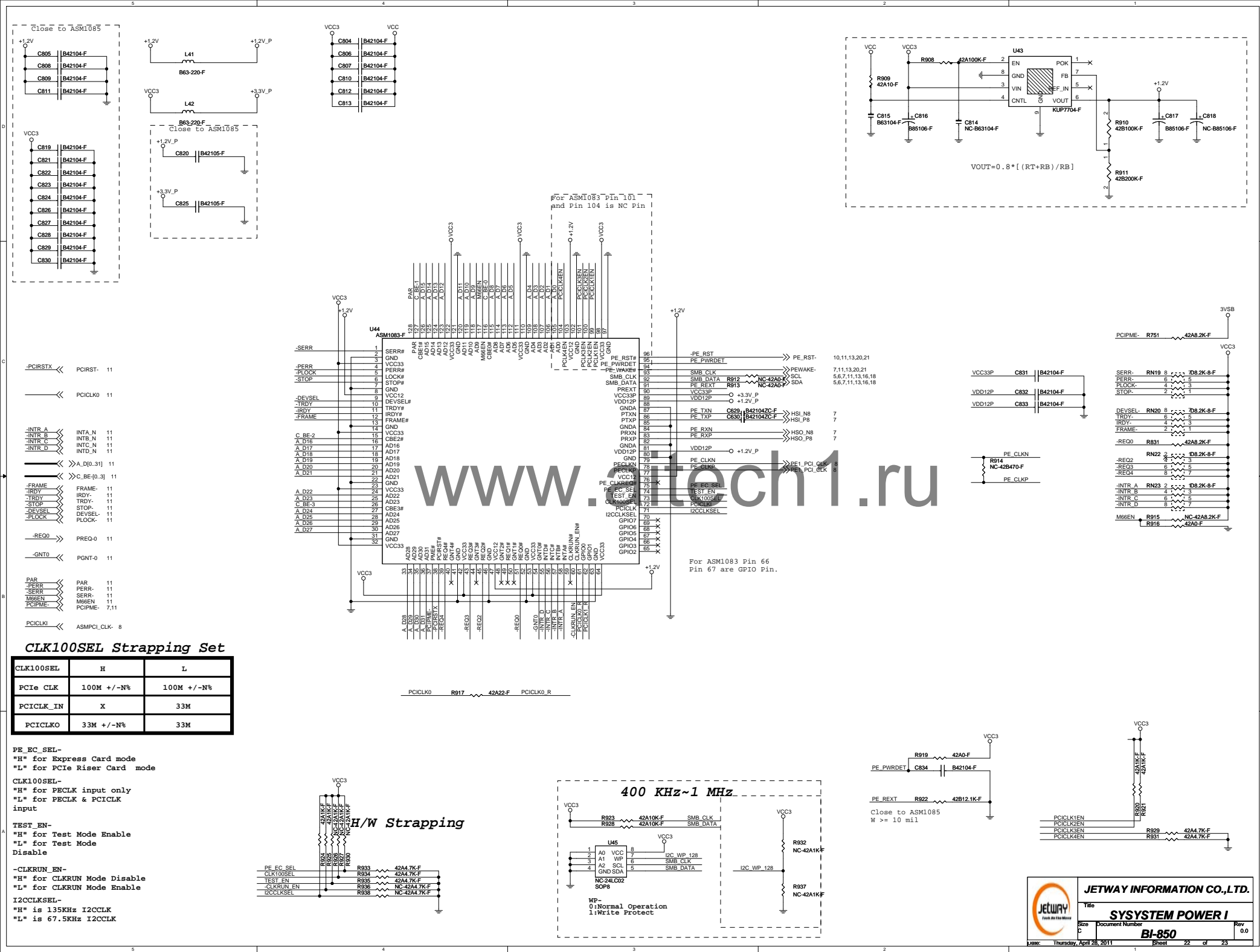
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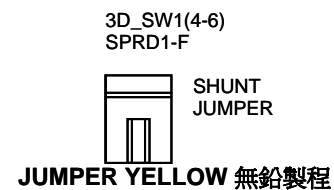
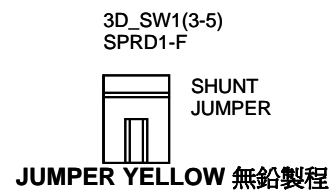
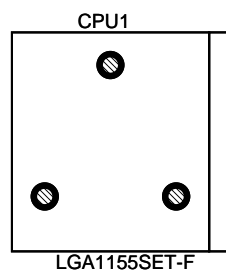
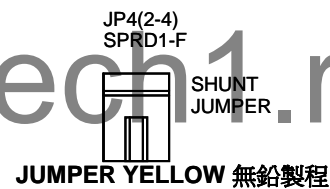
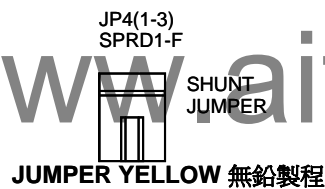
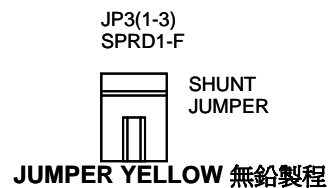
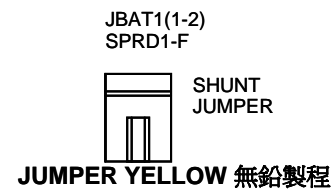
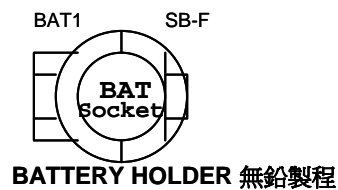
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Document Number: **BI-850**

Date: Thursday, April 28, 2011

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		Title ACCESSORY	
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Date: Friday, March 25, 2011		Sheet 23	of 23